

5 a package having a pair of opposed upright ends, said package connected to any of said at
6 least one battery and said at least one integrated circuit chip.

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1 5. (As Amended) A system level device for battery and integrated circuit chip integration
2 comprising:

3 at least one battery;
4 at least one integrated circuit chip powered by said at least one battery; and
5 a package connected to any of said at least one battery and said at least one integrated
6 circuit chip,

7 wherein said at least one battery connects to a pair of opposed upright ends of said
8 package.

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1 7. (As Amended) A system level device for battery and integrated circuit chip integration
2 comprising:

3 a multi-chip module integration system, wherein said multi-chip module integration
4 system comprises:

5 a multi-chip module having a pair of opposed upright ends;
6 at least one battery connected to said multi-chip module; and
7 at least one integrated circuit chip connected to said battery, wherein said
8 integrated circuit chip is powered by said battery.

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1 10. (As Amended) A system level device for battery and integrated circuit chip integration
2 comprising:

3 a multi-chip module integration system, wherein said multi-chip module integration
4 system comprises:
5 a multi-chip module;
6 at least one battery connected to said multi-chip module; and
7 at least one integrated circuit chip connected to said battery, wherein said
8 integrated circuit chip is powered by said battery,
9 wherein said at least one battery connects to a pair of opposed upright ends of said multi-
10 chip module.

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1 11. (As Amended) An integrated chip structure comprising:
2 an integrated circuit chip;
3 a battery directly connected to said integrated circuit chip; and
4 a package having a pair of opposed upright ends, said package connected to any of said
5 battery and said integrated circuit chip.

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1 14. (As Amended) The structure in claim 11, wherein said package surrounds said battery
2 and said integrated circuit chip.

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1 16. (As Amended) An integrated chip structure comprising:
2 a package having a pair of opposed upright ends;
3 an integrated circuit chip mounted on said package; and
4 a battery directly connected to said package and electrically connected to said integrated
5 circuit chip.